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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al.

Art Unit : 2815

Serial No.: 09/379,702

Examiner : Eugene Lee

Filed : August 24, 1999

Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICES

Commissioner of Patents

Washington, D.C. 20231

REQUEST FOR DRAWING CORRECTION

In FIG. 1D, please change the label 108 to read --104--.

Entry is shown in red on the attached FIG. 1D and is respectfully requested. This change conforms the drawings to comport with the specification.

Please apply any charges or credits to Deposit Account

No. 06-1050.

Respectfully submitted,

Date: 4/12/01

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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